

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Luiz Andre Barroso et al.

Parent Serial No. 09/877,793

Parent Appln. Filed: June 8, 2001

Serial No.: Unassigned

Filed: Herewith

For: A Scalable Architecture Based on
Single-Chip Multiprocessing

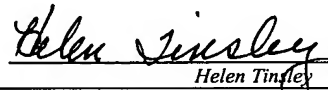
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Group Art Unit: 2186

Examiner: Li, Z.

Atty. Docket: 200301825-5
(NUHP:0172-1/FLE)

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
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Date	Helen Tinsley

Sir:

PRELIMINARY AMENDMENT

Prior to examination of the above-referenced application, please enter the amendment and consider the remarks set forth below.